

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Gilbert Wolrich et al.	Art Unit	: 2183
Assignee	: Intel Corporation	Examiner	: Daniel H. Pan
Serial No.	: 10/070,091	Conf. No:	: 7309
Filed	: February 27, 2002		
Title	: REGISTER SET USED IN MULTITHREADED PARALLEL PROCESSOR ARCHITECTURE		

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO FINAL ACTION OF SEPTEMBER 5, 2006

Please amend the above-identified application as follows: